

**Complete if Known**

<b>Application Number</b>	10/734,231
<b>Filing Date</b>	Dec 15, 2003
<b>First Named Inventor</b>	PROOS, John A. et al.
<b>Art Unit</b>	NOT YET SPECIFIED
<b>Examiner Name</b>	NOT YET SPECIFIED
<b>Attorney Docket Number</b>	T00001-0466-US

*(use as many sheets as necessary)*

Sheet	1	of	2
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[illegible][illegible]Date  
Considered

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## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Sheet	2	of	2
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Attorney Docket Number	T00001-0466-US
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[illegible]Date  
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<sup>1</sup> Applicant's unique citation designation number (optional). <sup>2</sup> Applicant is to place a check mark here if English language Translation is attached. This collection of information is required by 37 CFR 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 120 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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Substitute for form 1449B/PTO		<b>Complete if Known</b>	
		Application Number	10/734,231
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)		Filing Date	December 15, 2003
		First Named Inventor	PROOS
		Group Art Unit	NOT YET SPECIFIED
		Examiner Name	NOT YET SPECIFIED
		Attorney Docket Number	T00001-0466-US
Sheet	1	of	1

OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS			
Examiner Initials <sup>1</sup>	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		Consortium for Efficient Embedded Security, EESS #1: Implementation Aspects of NTRUEncrypt and NTRUSign, Version 1, November 2002.	
		HESS, E; JANSSEN, N; MEYER, B; SCHUETZE, T. "Information Leakage Attacks Against Smart Card Implementations of Cryptographic Algorithms and Countermeasures - A Survey", Proceedings of Eurosmart Security Conference, pp. 55-64, Marseilles, 2000, European Smart Card Industry Assoc., 2000.	
		SHAMIR, A. "Protecting Smart Cards from Passive Power Analysis with Detached Power Supplies", in Proceedings of Cryptographic Hardware and Embedded Systems - CHES 2000, pp. 71-77, LNCS Vol. 1965, C.K. Koc et al, Eds., Springer-Verlag, 2000.	
		KOCHER, P.C. "Timing Attacks on Implementations of Diffie-Hellman, RSA, DSS, and Other Systems", in Advances in Cryptology - CRYPTO'96, LNCS Vol. 1109, N. Koblitz, Ed., pp. 104-113, Springer-Verlag, 1996.	

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